

Fig. 1

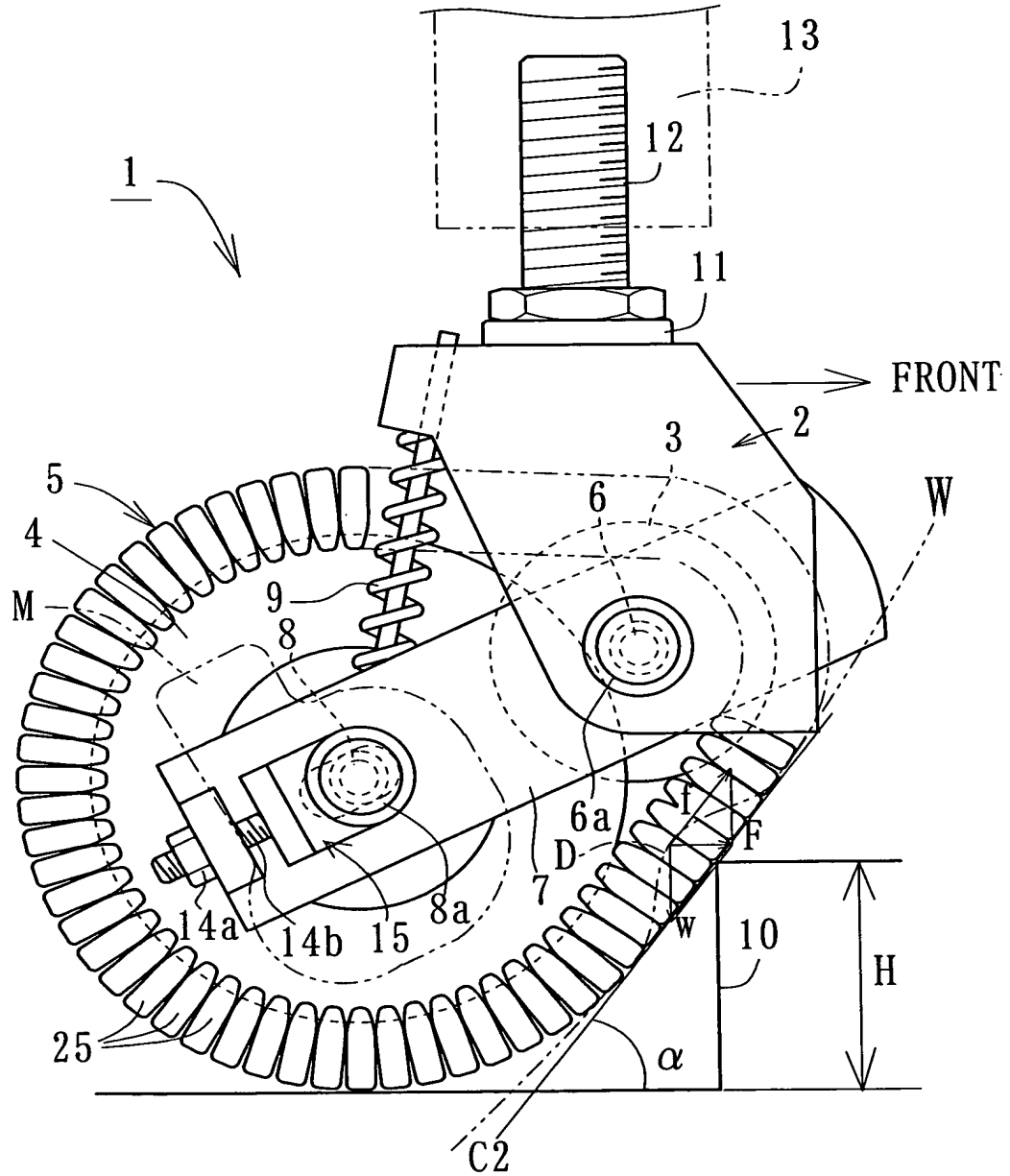


Fig. 2

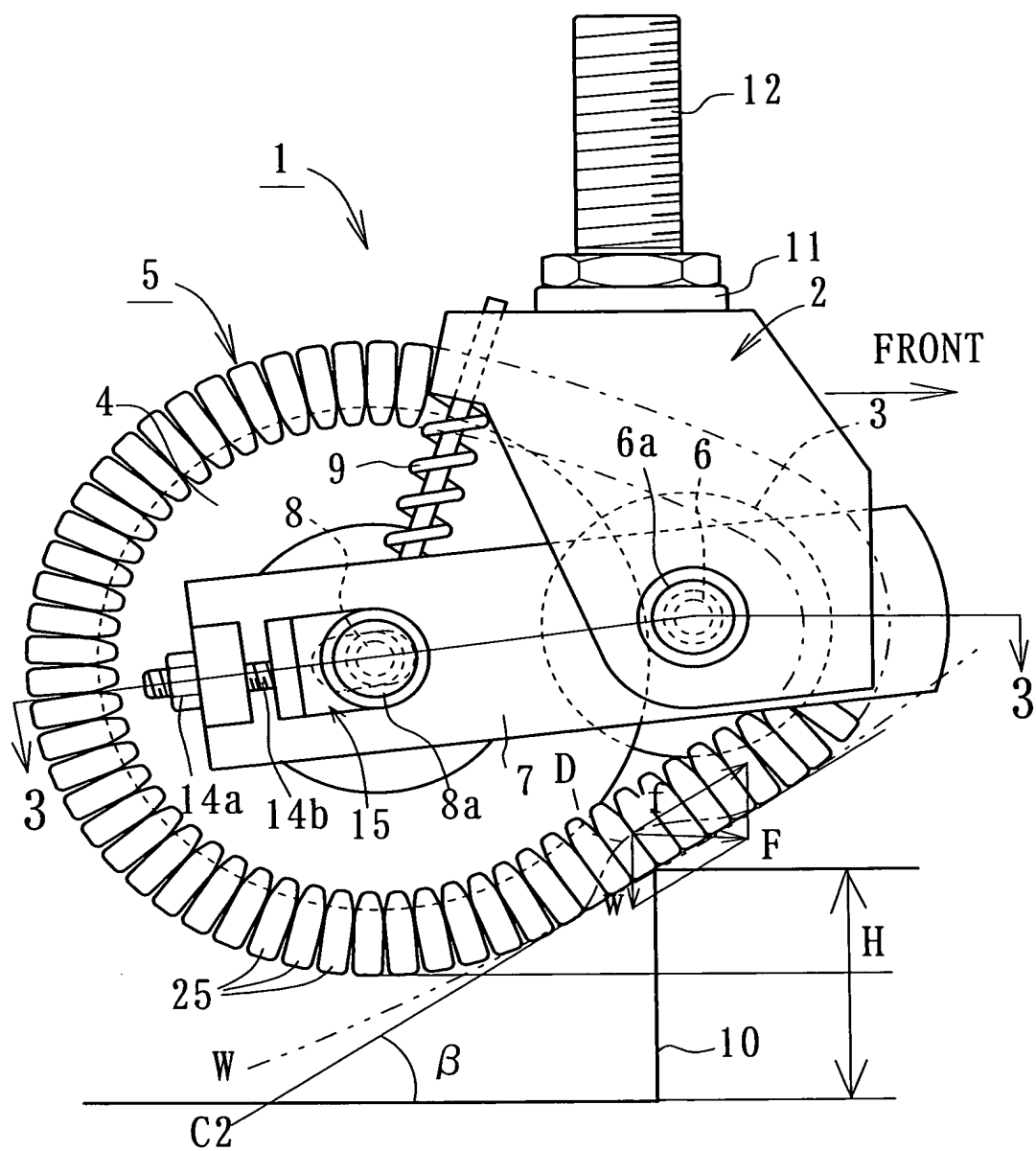


Fig. 3

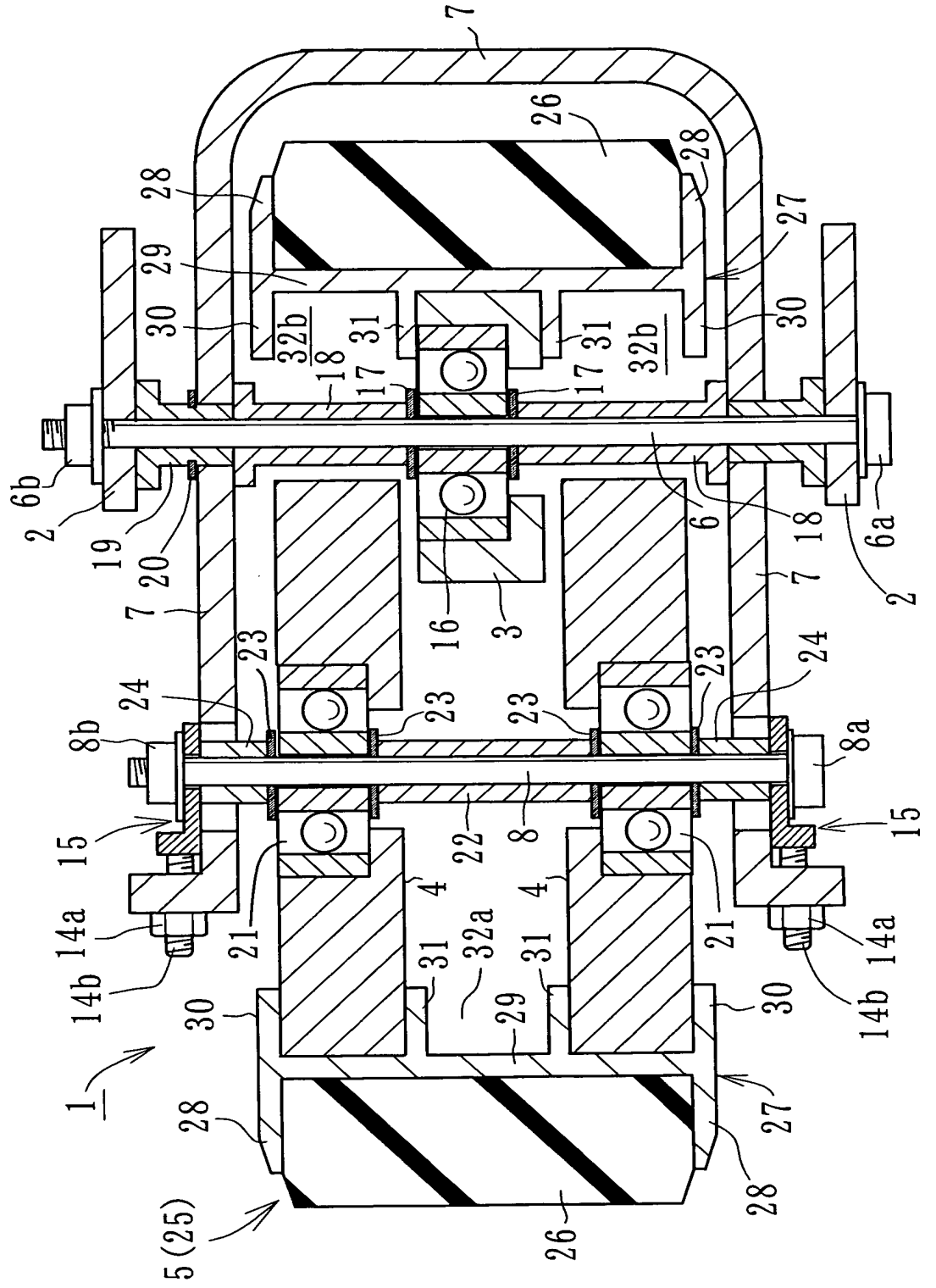


Fig. 4

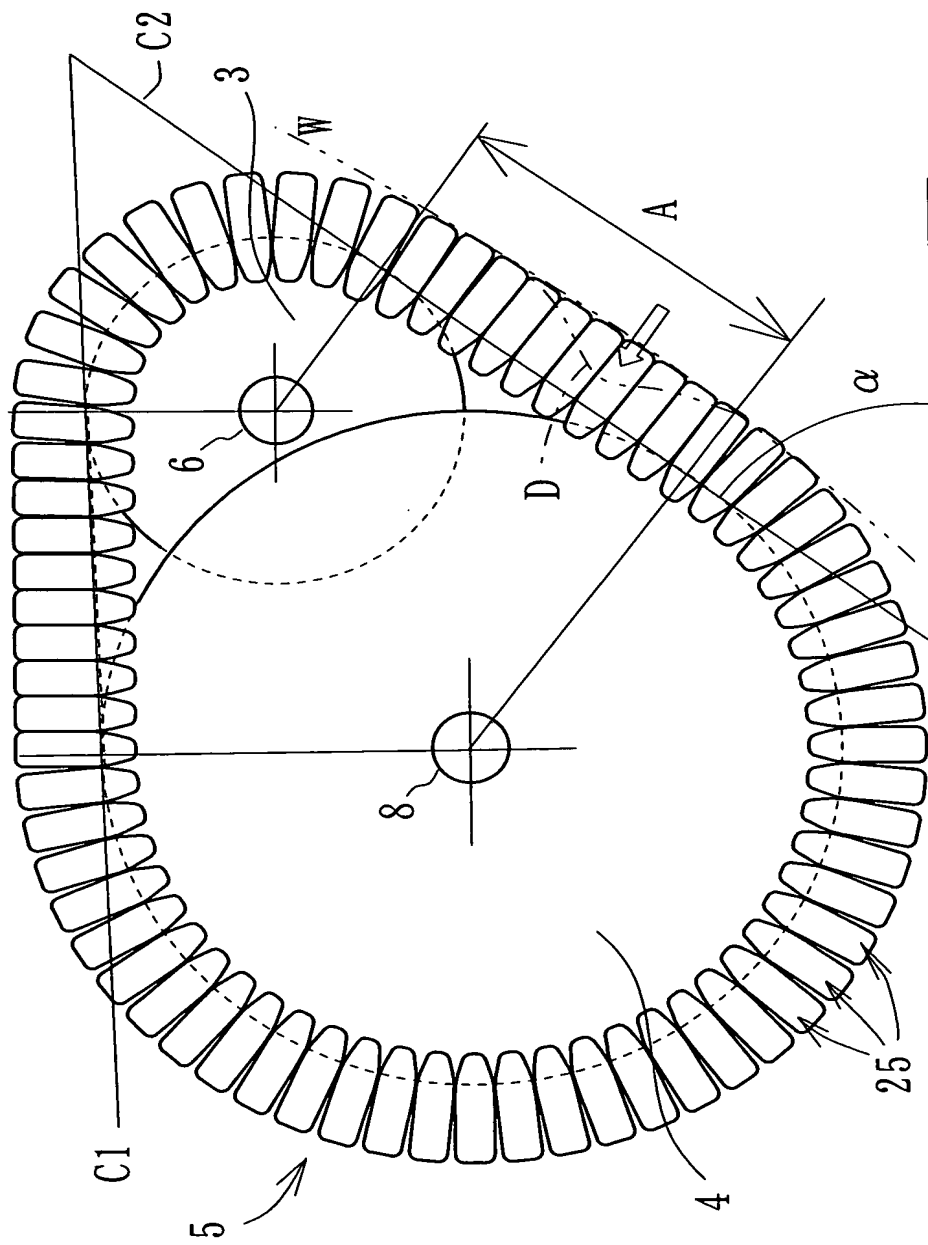


Fig. 5

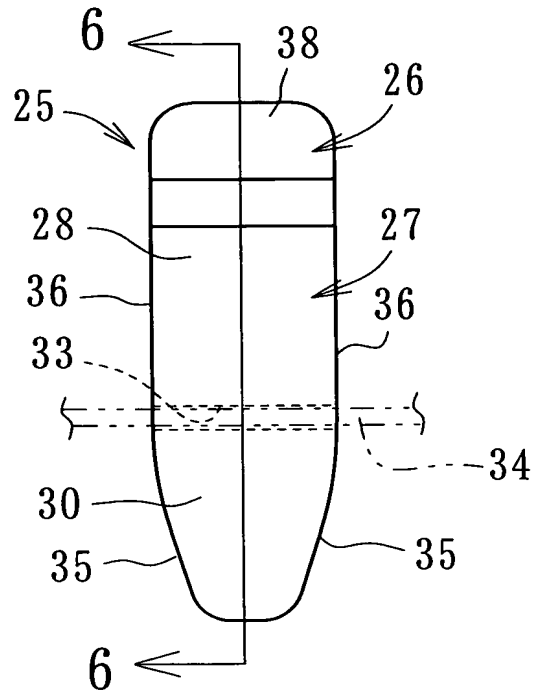


Fig. 6

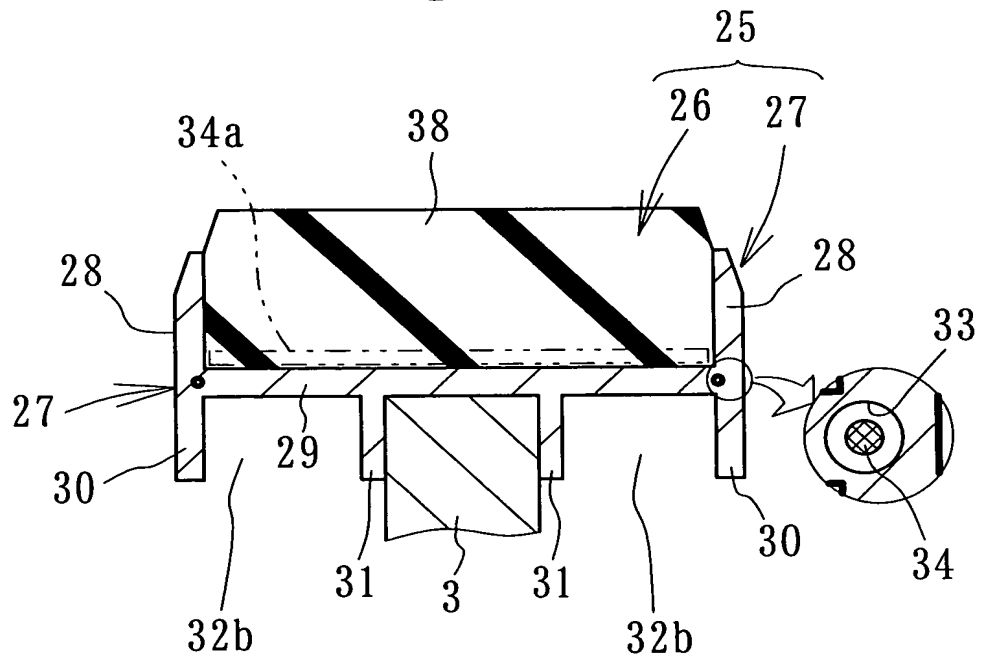
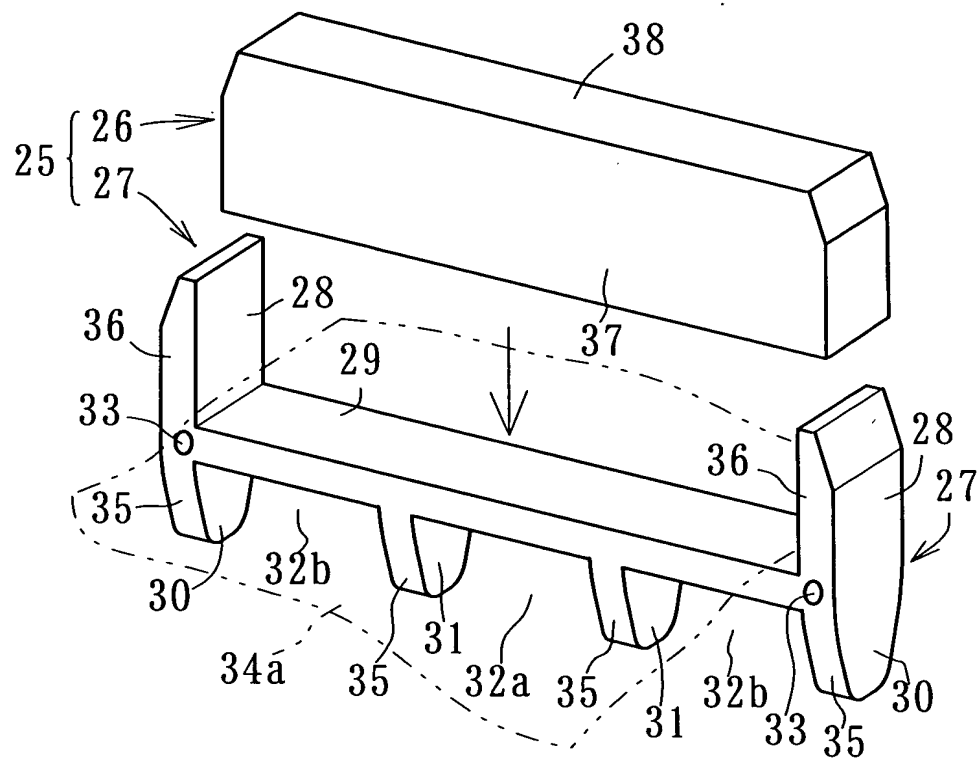
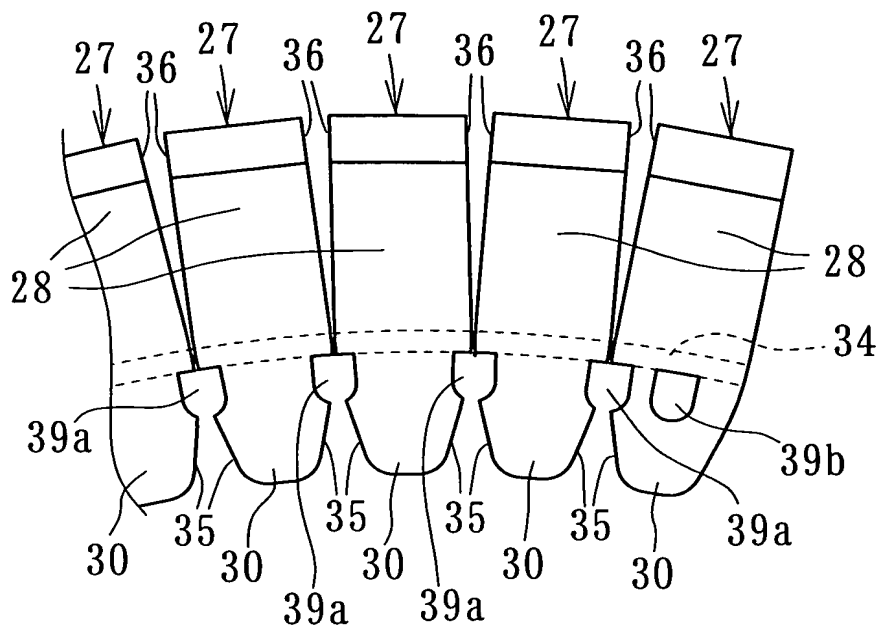


Fig. 7



# Fig. 8



# Fig. 9

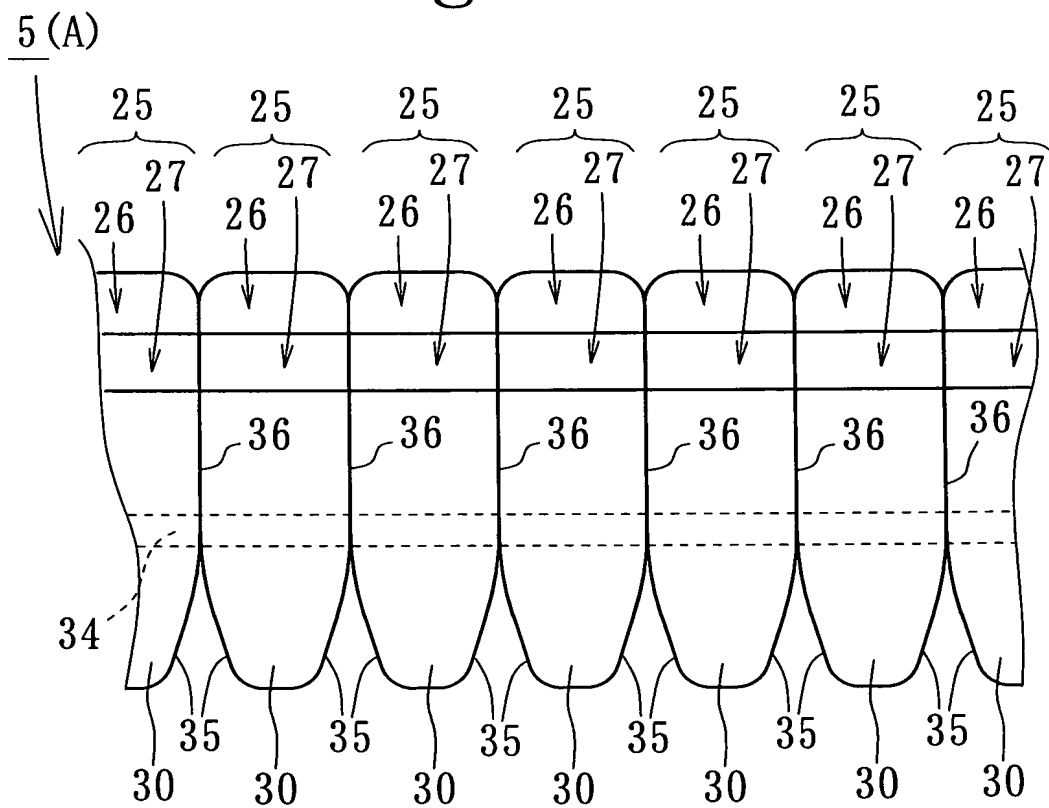


Fig. 10

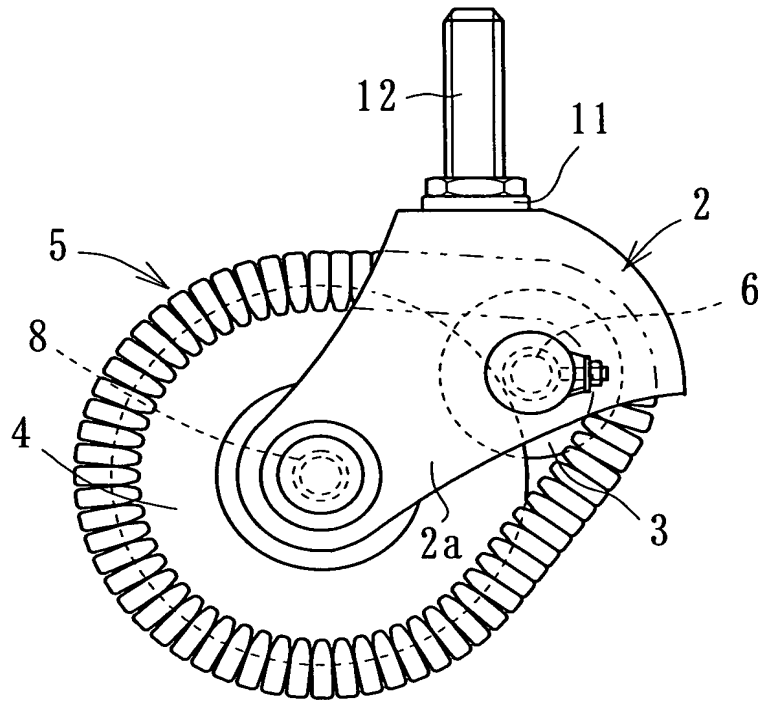


Fig. 11

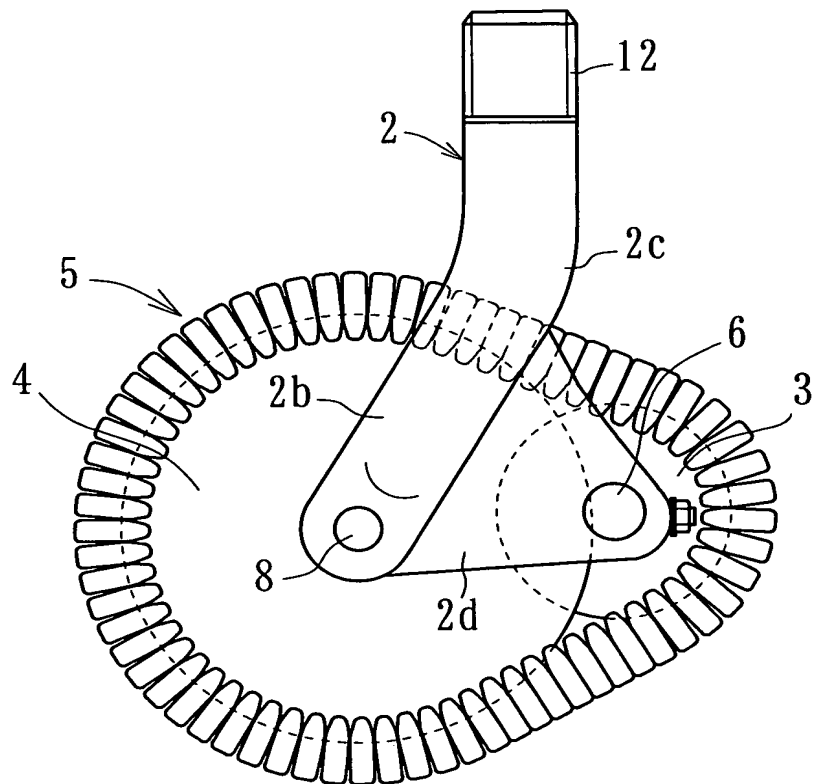




Fig. 1 2

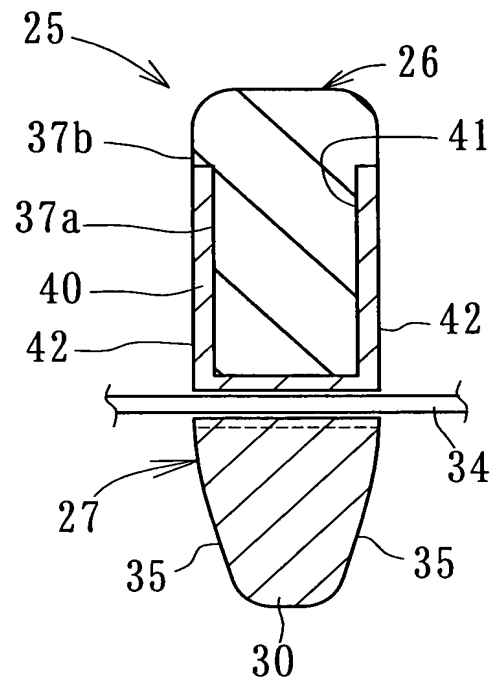


Fig. 1 3

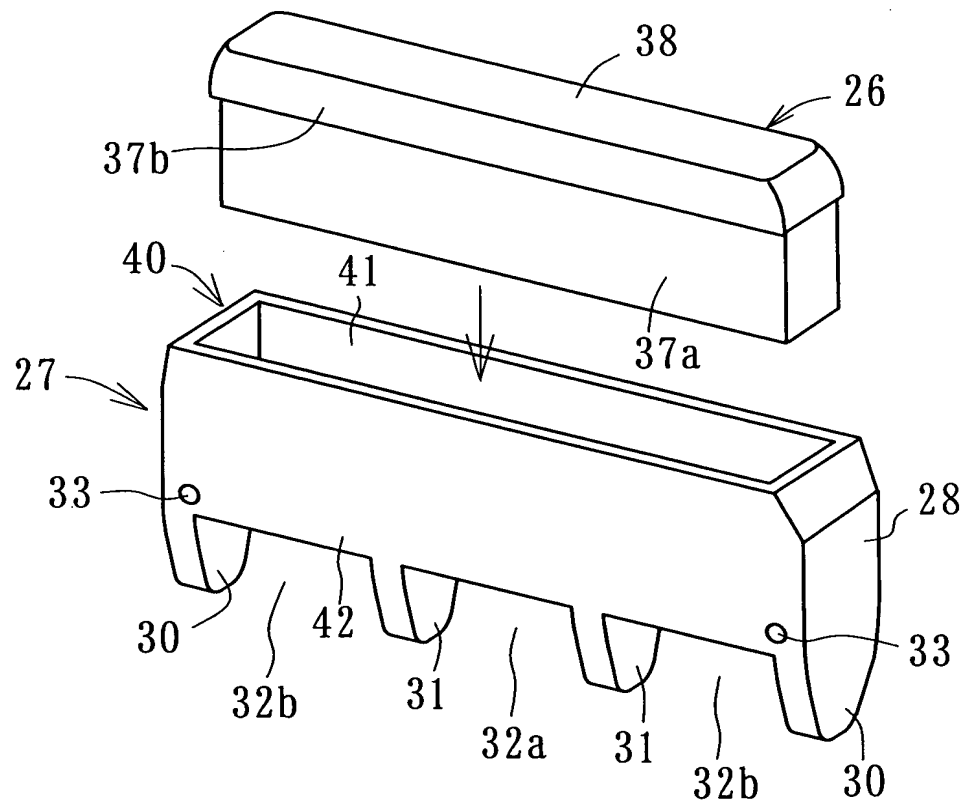


Fig. 1 4

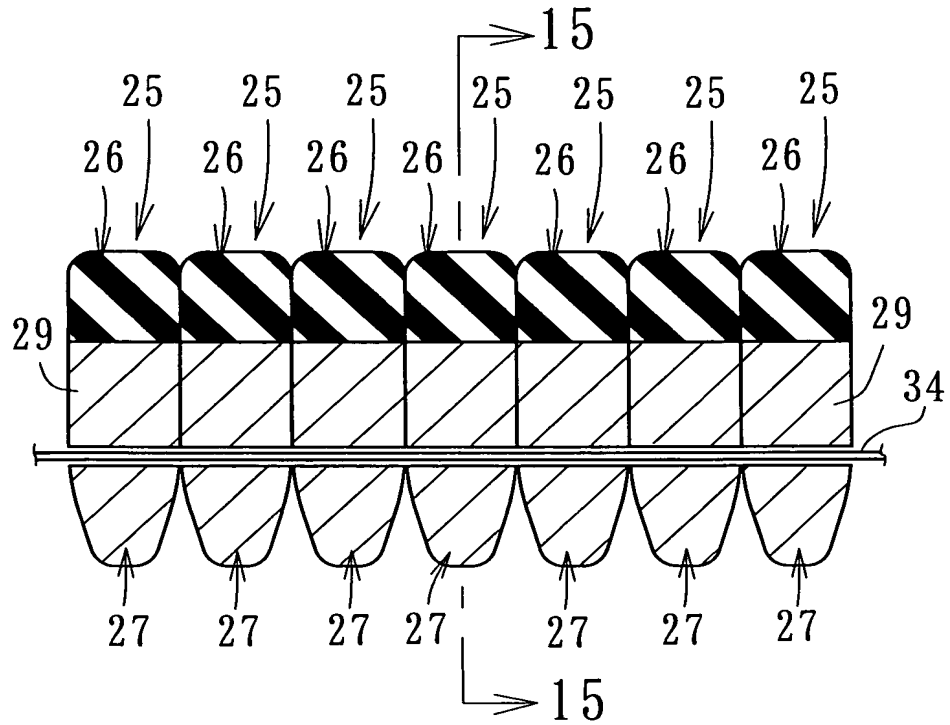


Fig. 1 5

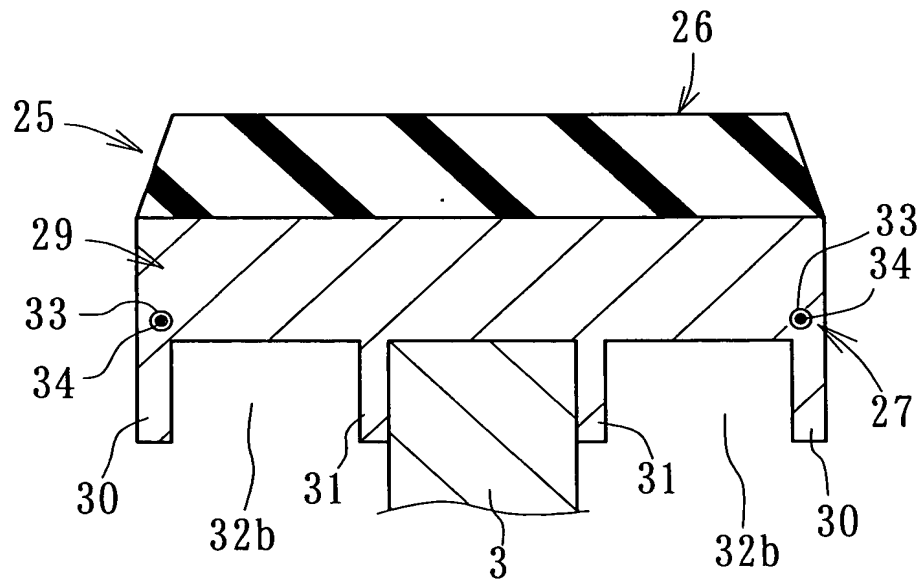


Fig. 1 6

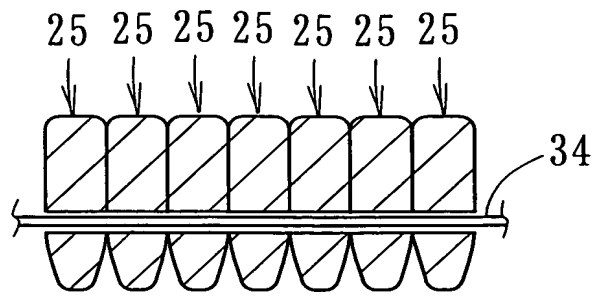


Fig. 1 7

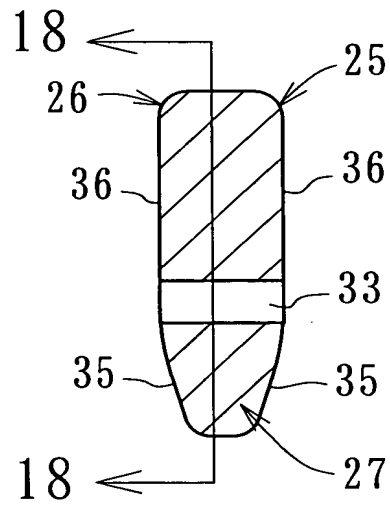


Fig. 1 8

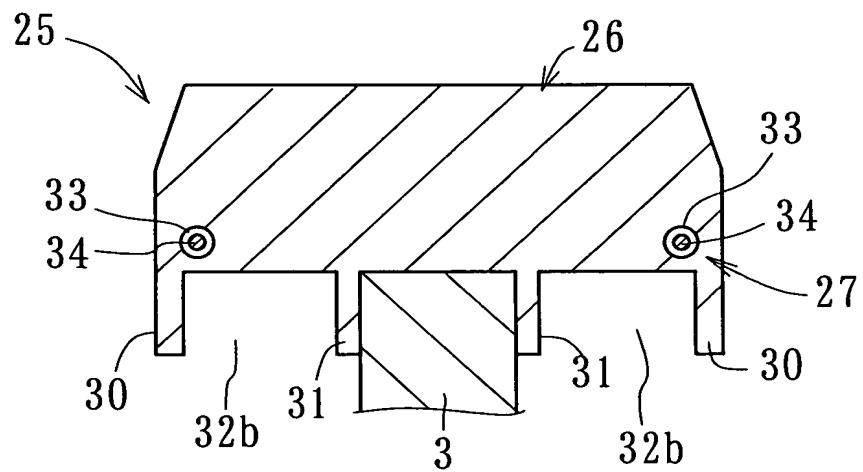


Fig. 1 9

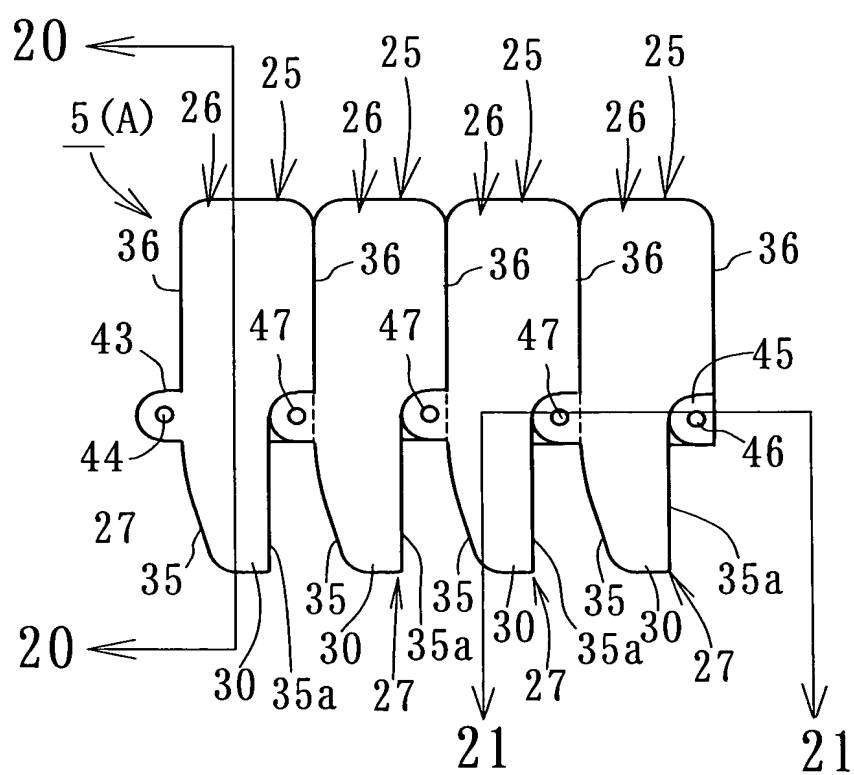


Fig. 1 is a cross-sectional view of a semiconductor device. The device features a substrate 25 with a top surface 26. A series of rectangular structures 30 are formed on the substrate. Each structure 30 consists of a base layer 31 and a top layer 32b. A central structure 3 is shown with a diagonal cross-section. The structures 30 are separated by gaps 32b. The top surface 26 is shown with a slight slope. The side walls of the structures 30 are labeled 43. The bottom surface of the substrate 25 is labeled 27.

Fig. 2 2

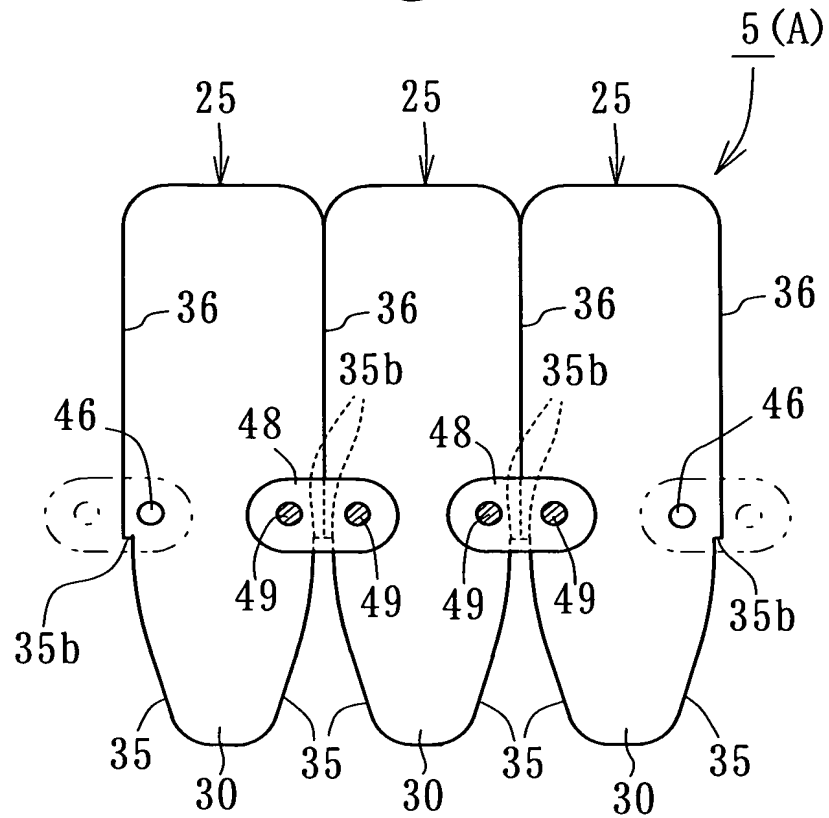


Fig. 2 3

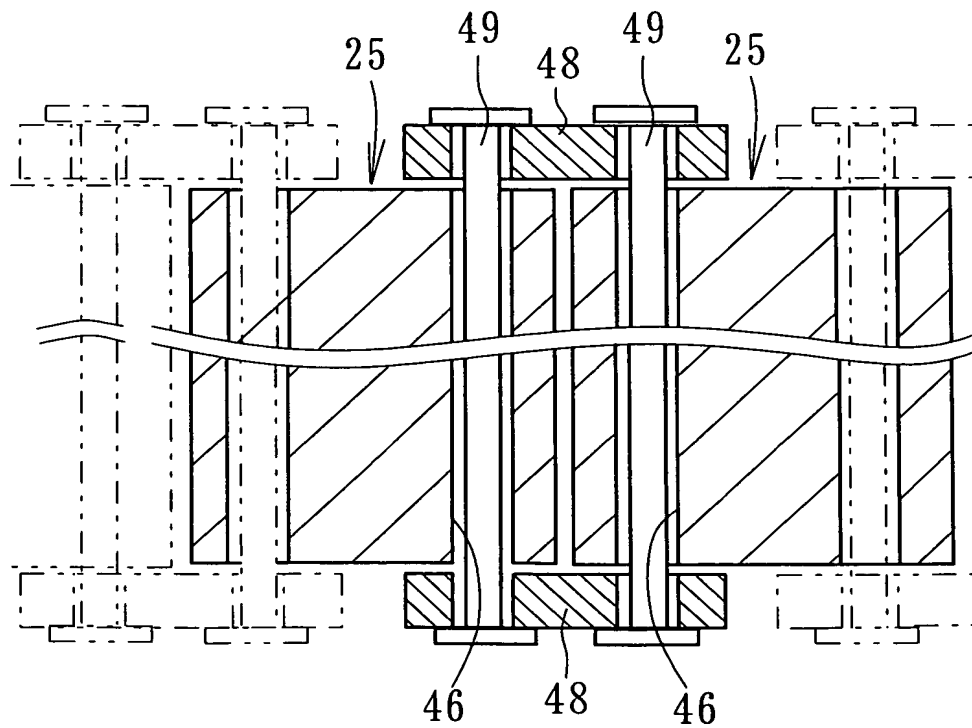
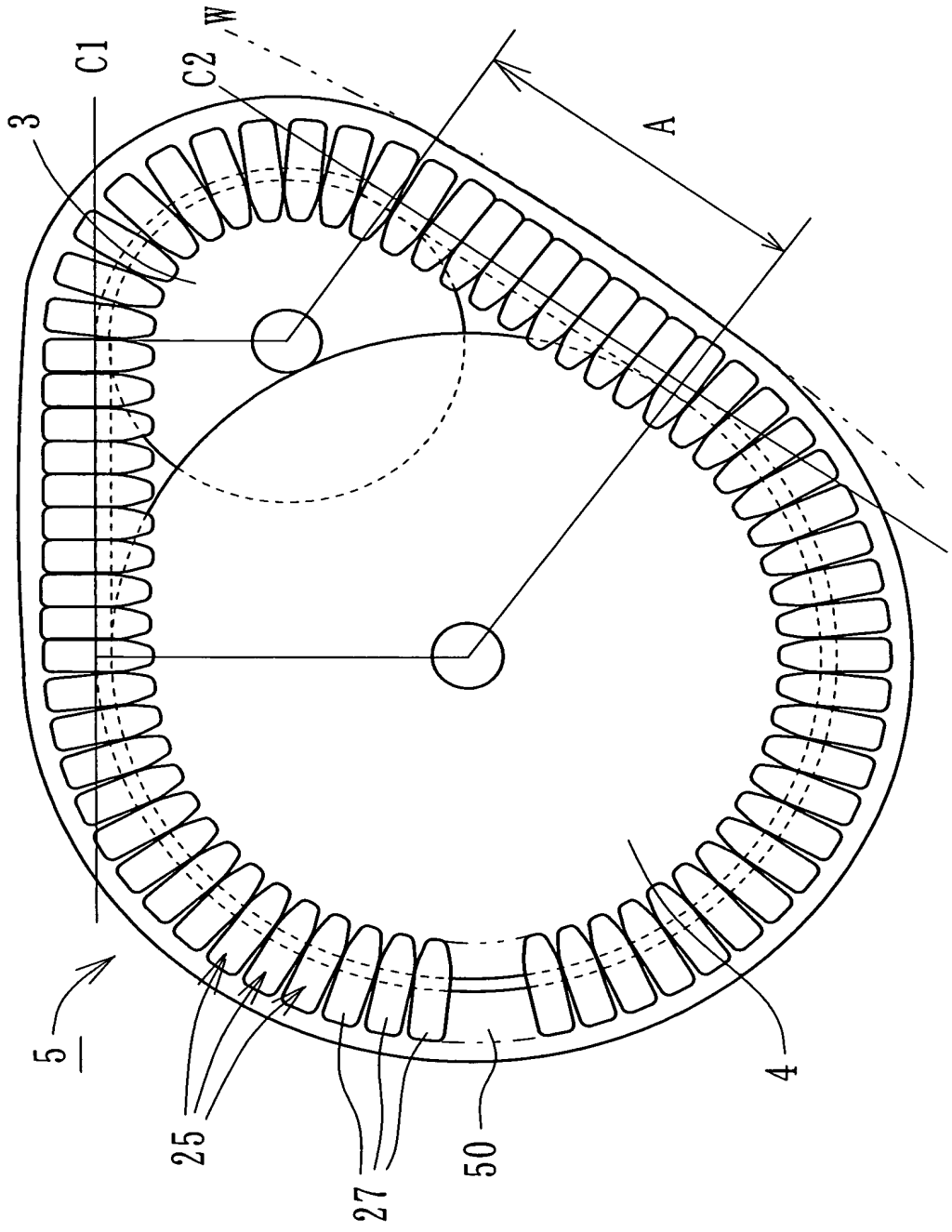
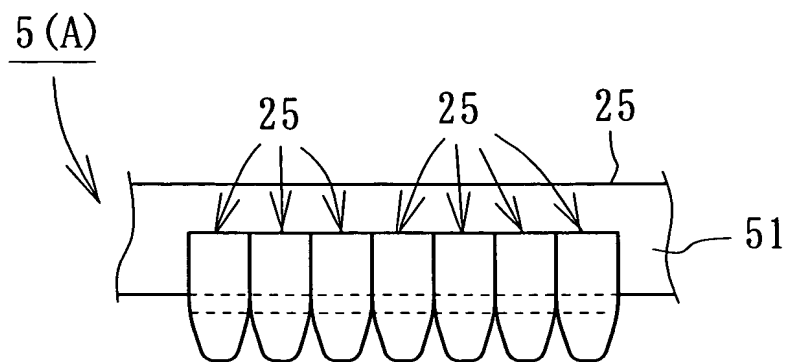


Fig. 2 4





# Fig. 2 5



# Fig. 2 6

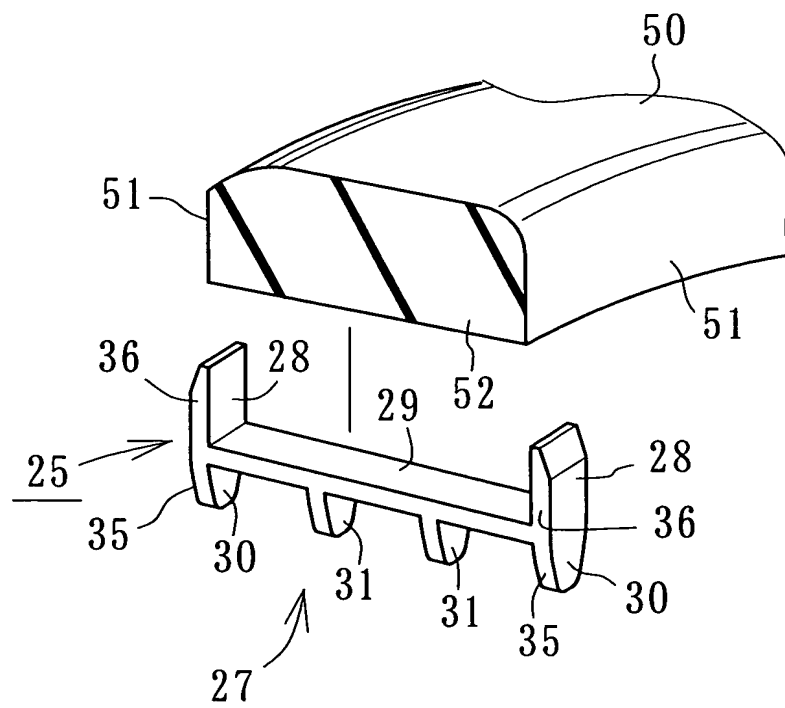


Fig. 2 7

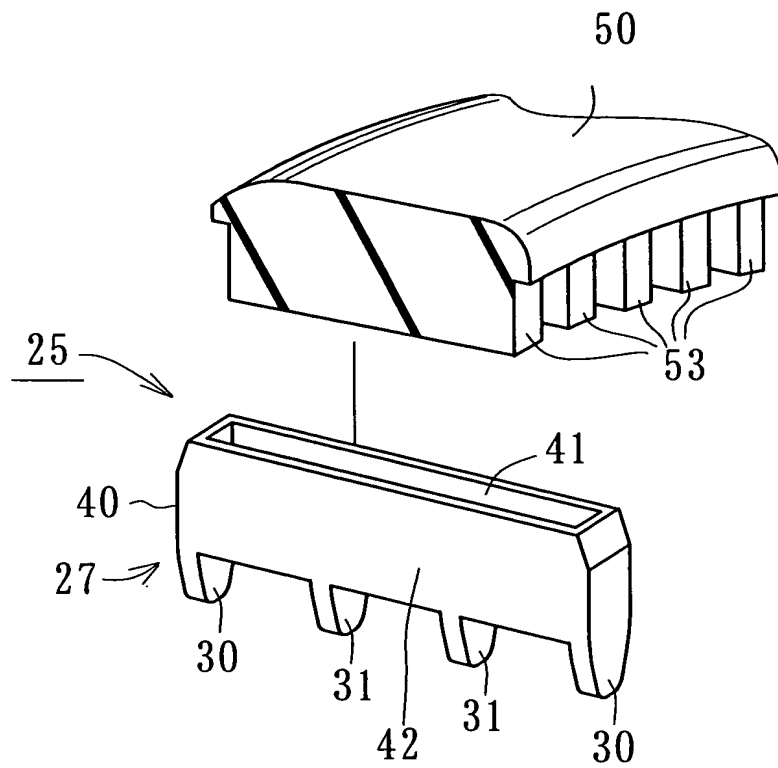


Fig. 2 8

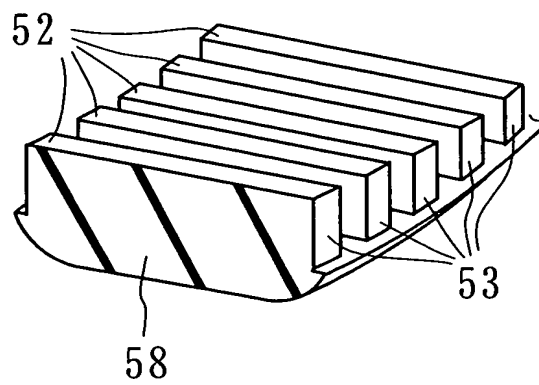


Fig. 2 9

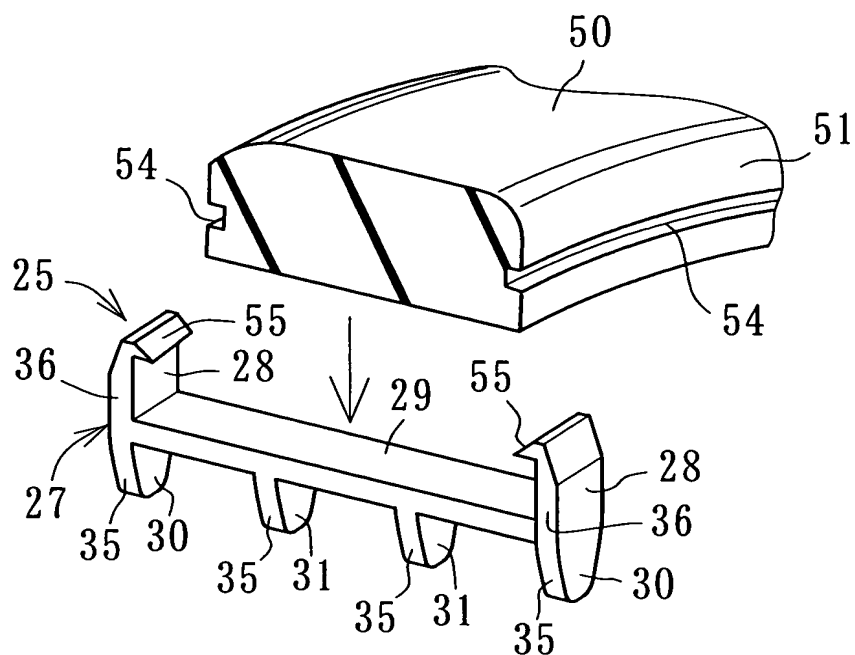


Fig. 3 0

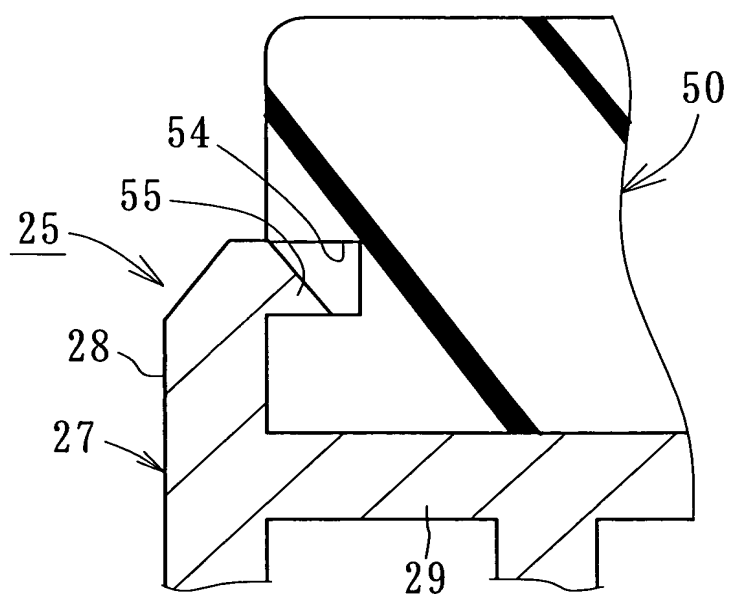


Fig. 3 1

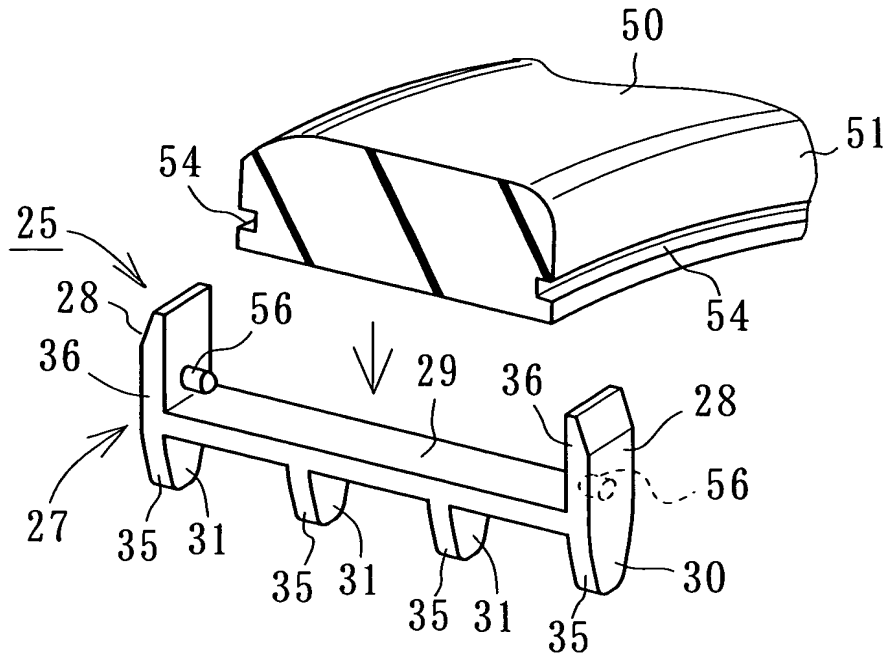


Fig. 3 2

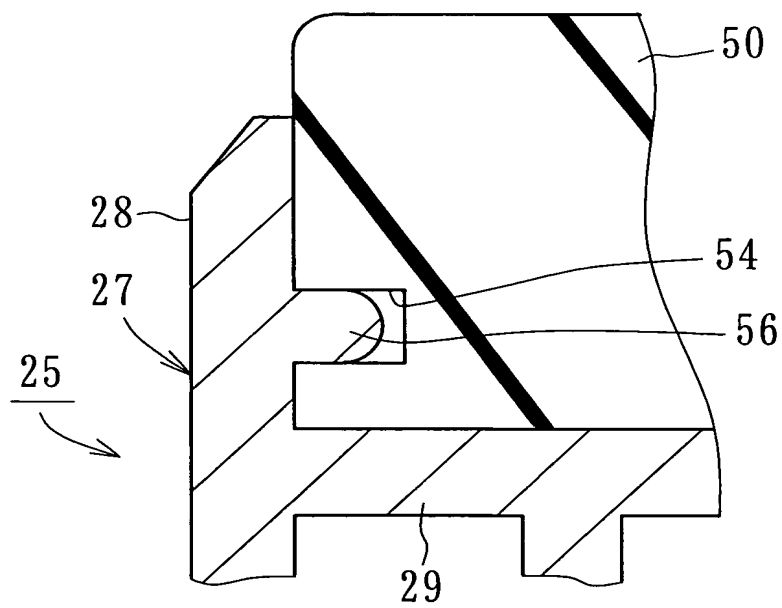


Fig. 3 3

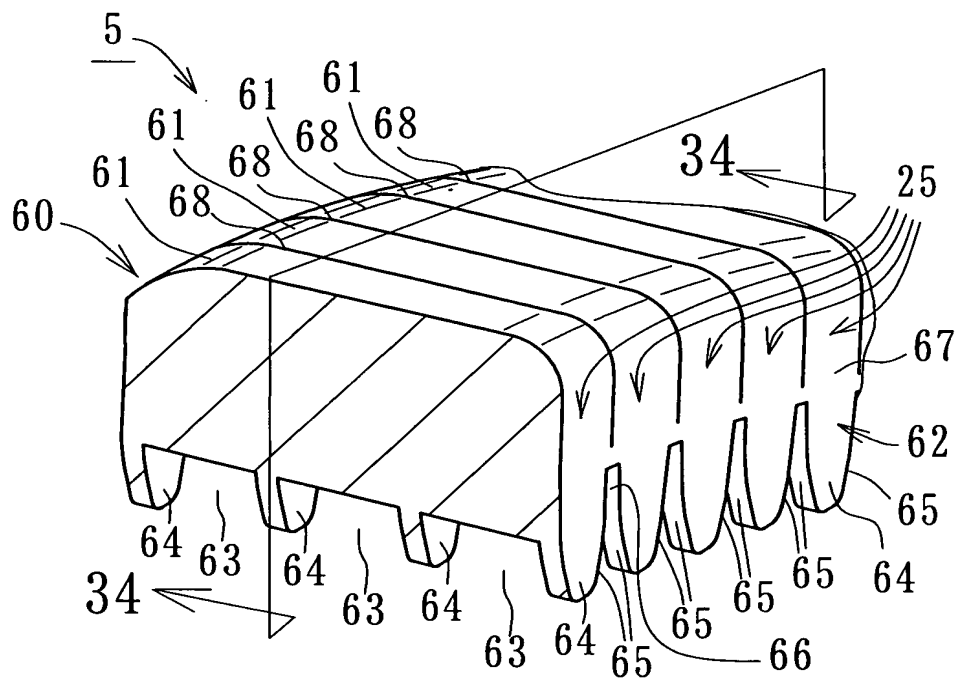


Fig. 3 4

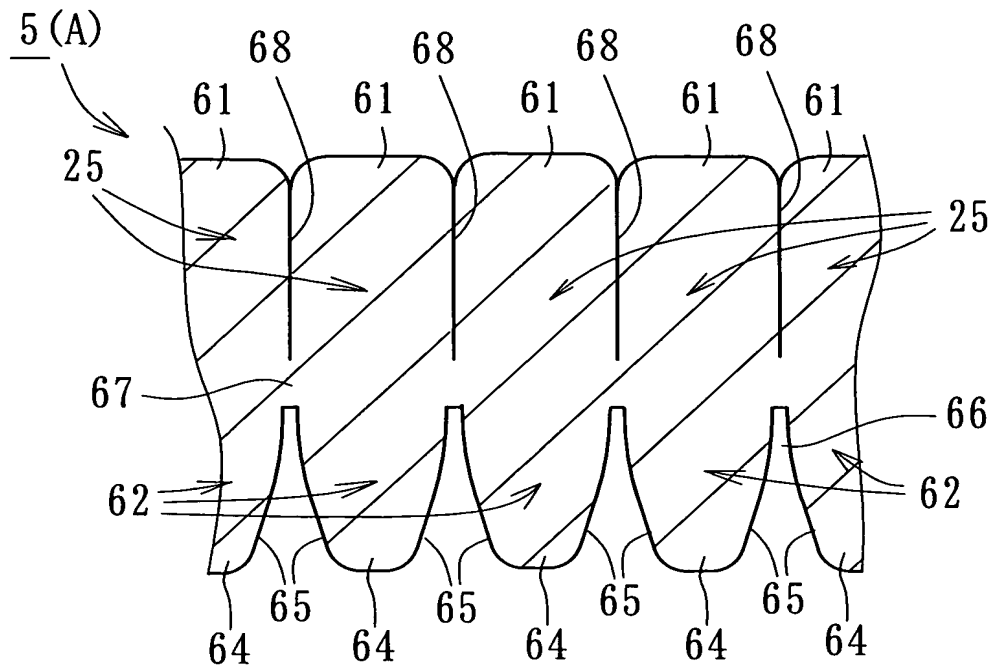


Fig. 3 5

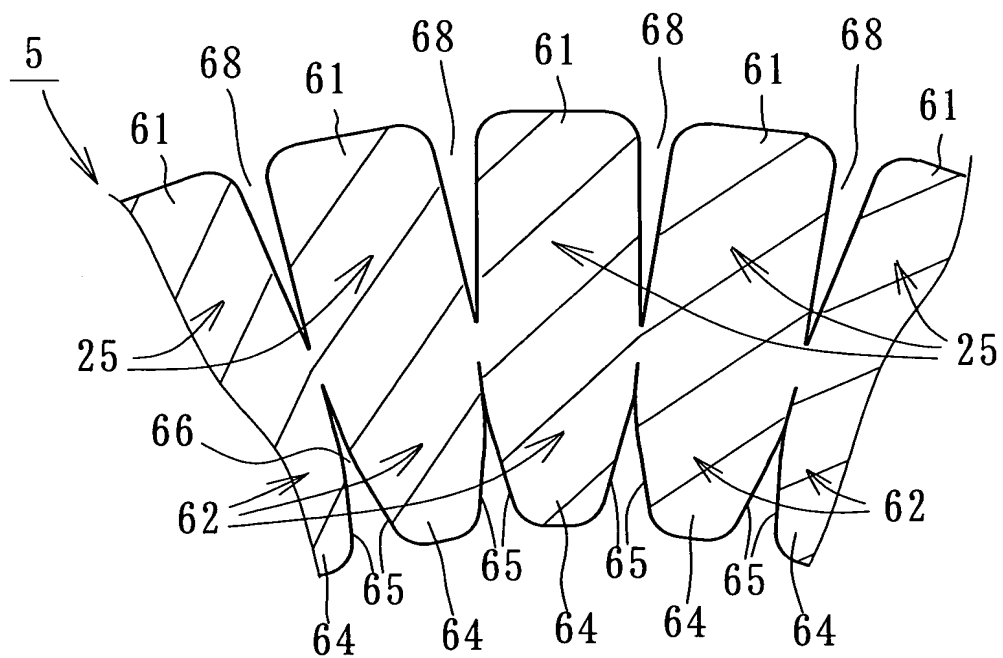


Fig. 3 6

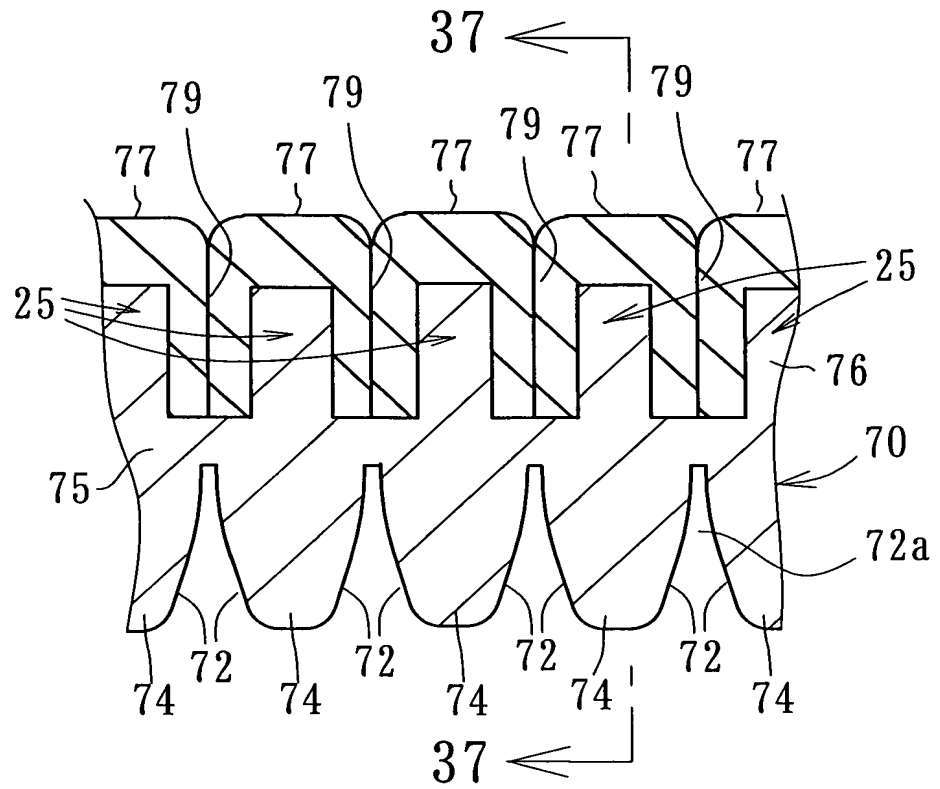


Fig. 3 7

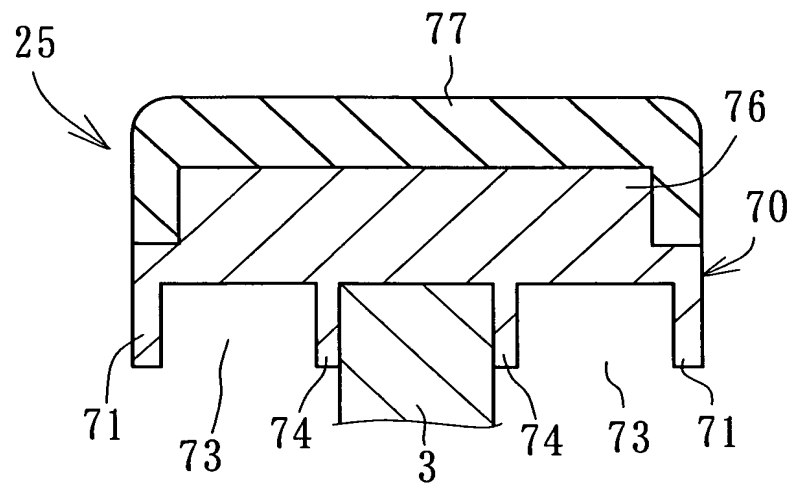


Fig. 3 8

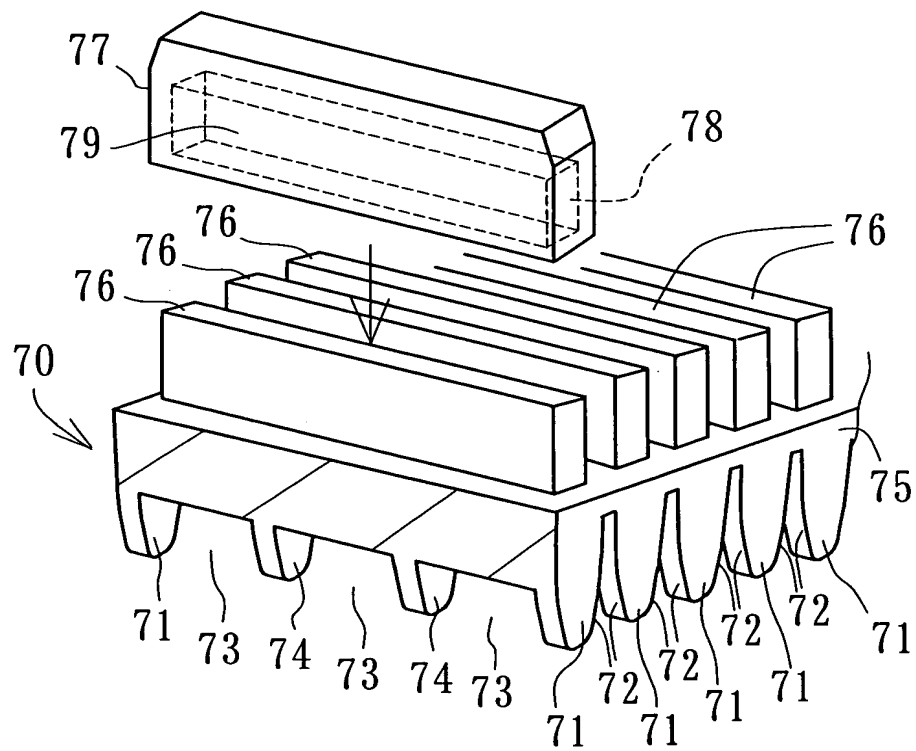




Fig. 3 9

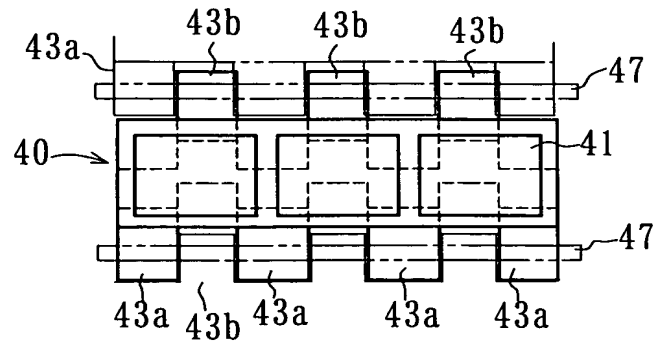


Fig. 4 0

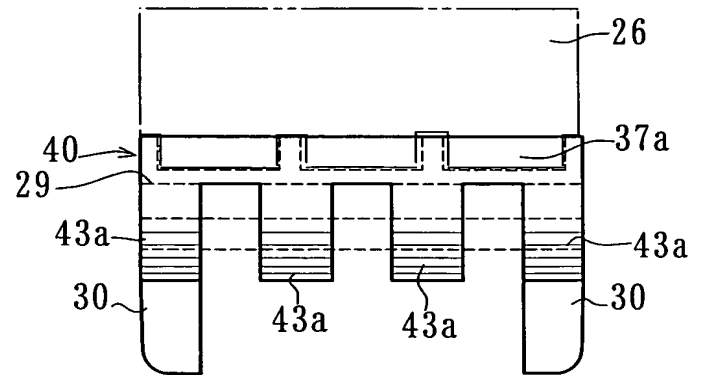


Fig. 4 1

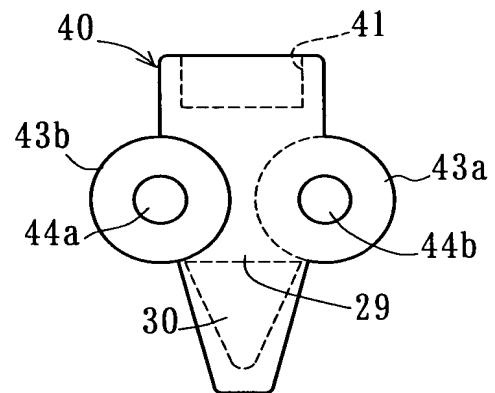


Fig. 4 2

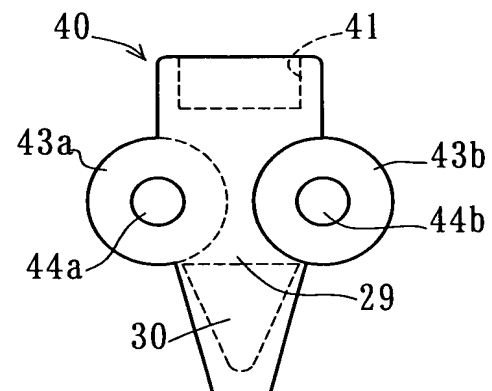


Fig. 4 3

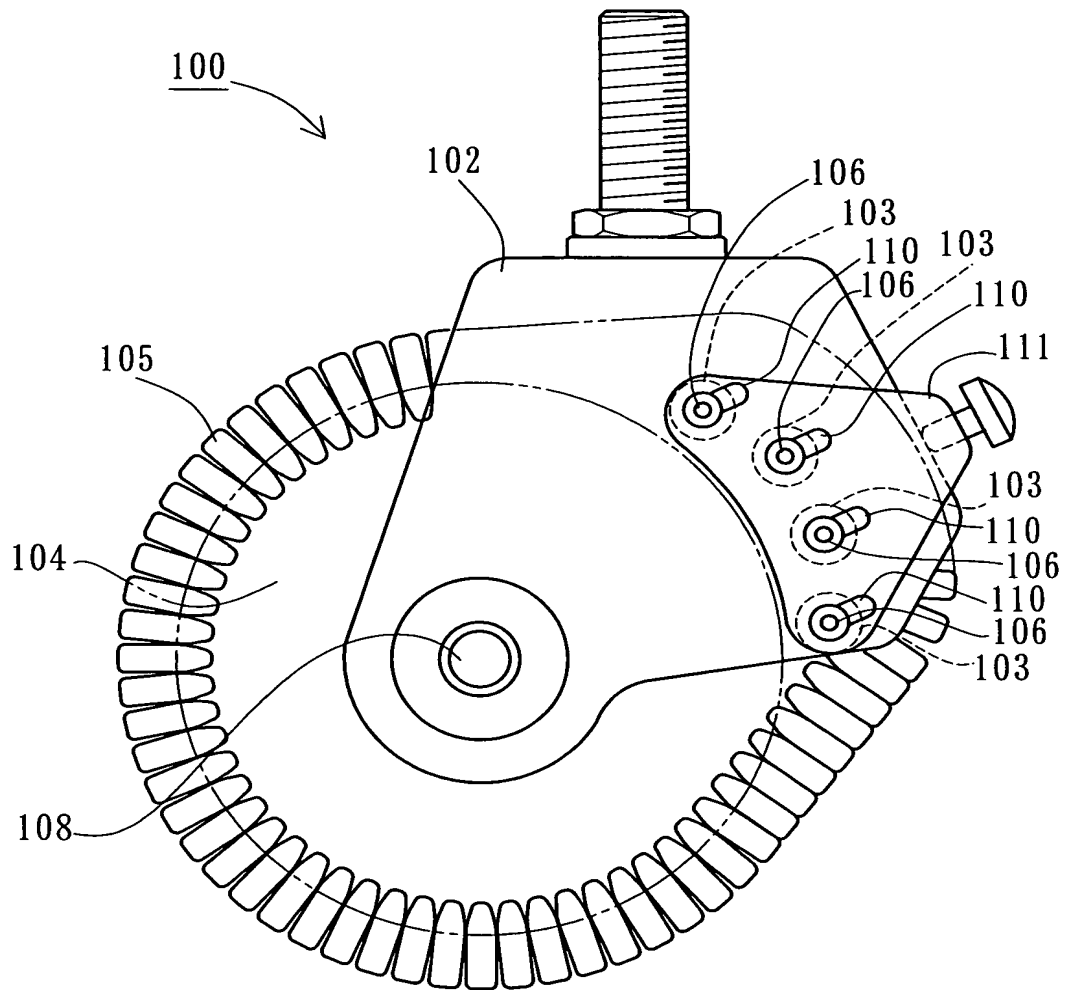


Fig. 4 4

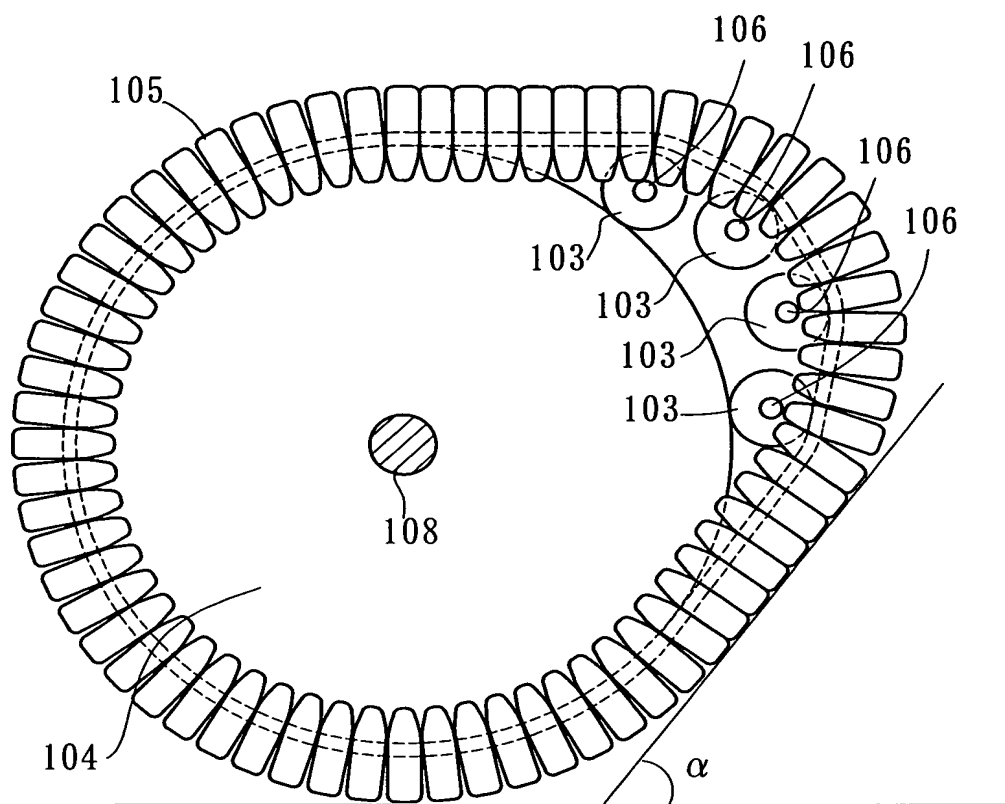


Fig. 4 5

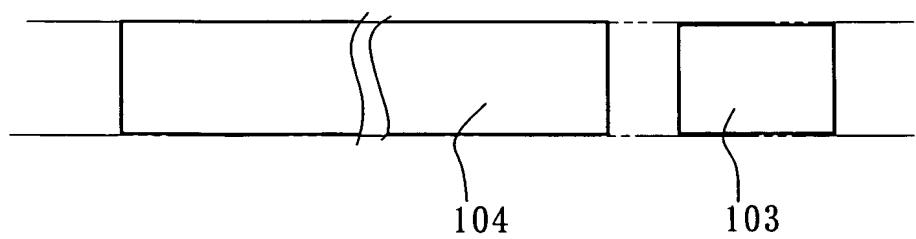


Fig. 4 6

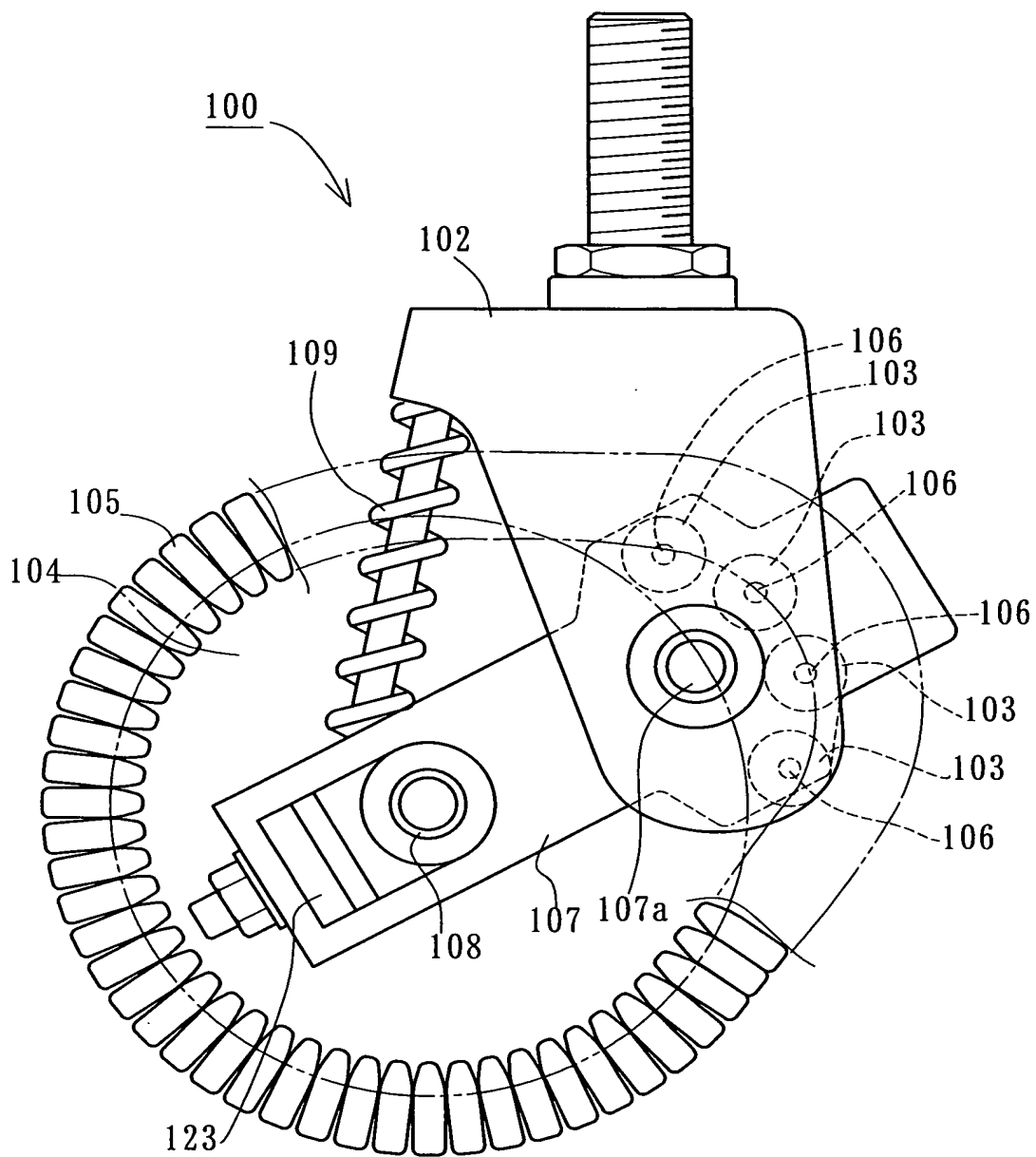


Fig. 4 7

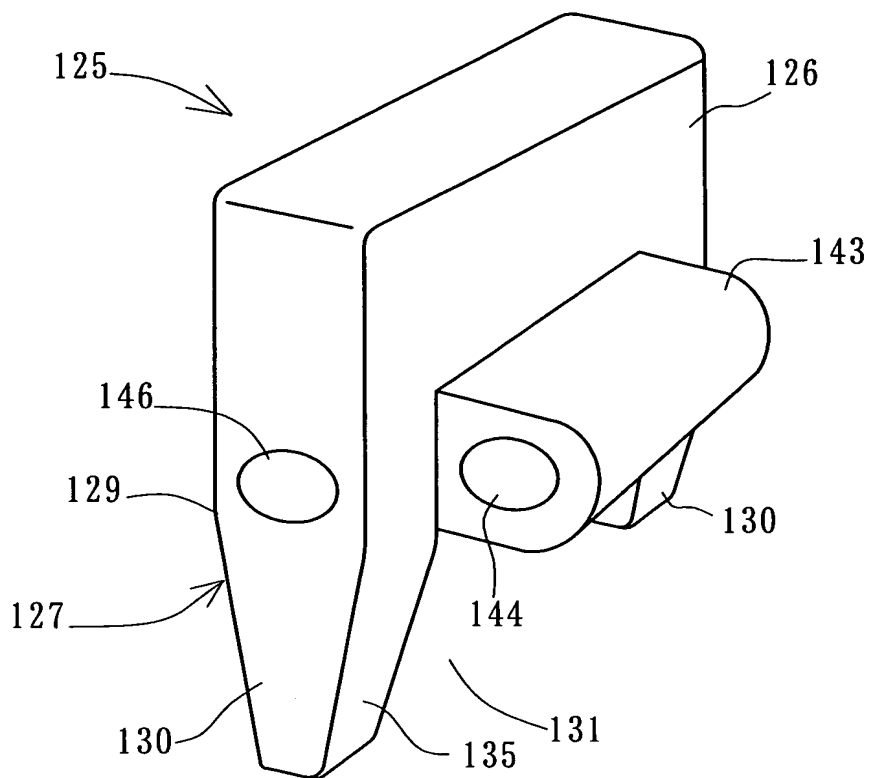


Fig. 4 8

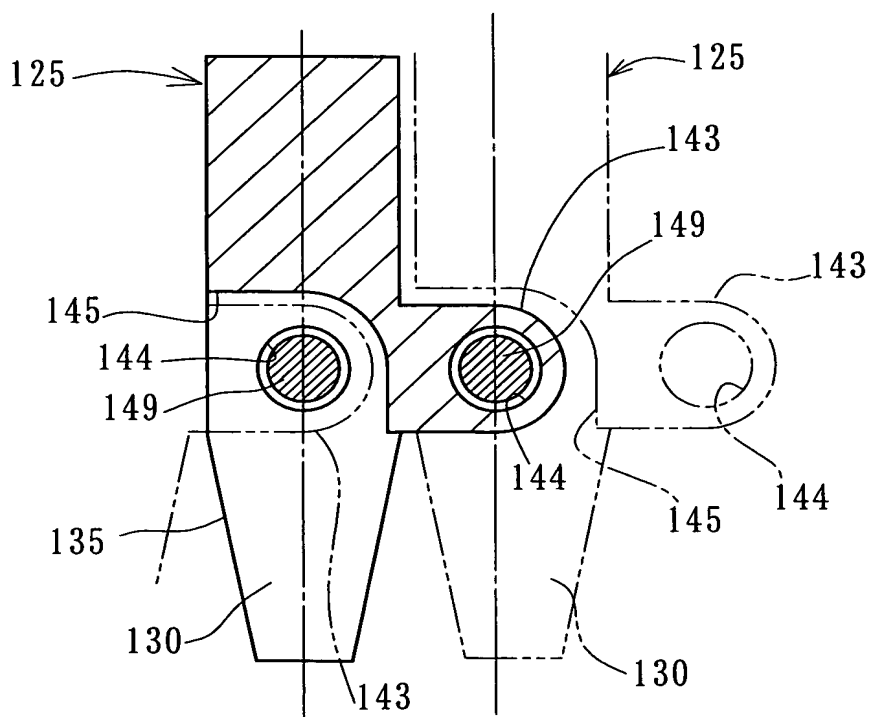


Fig. 4 9

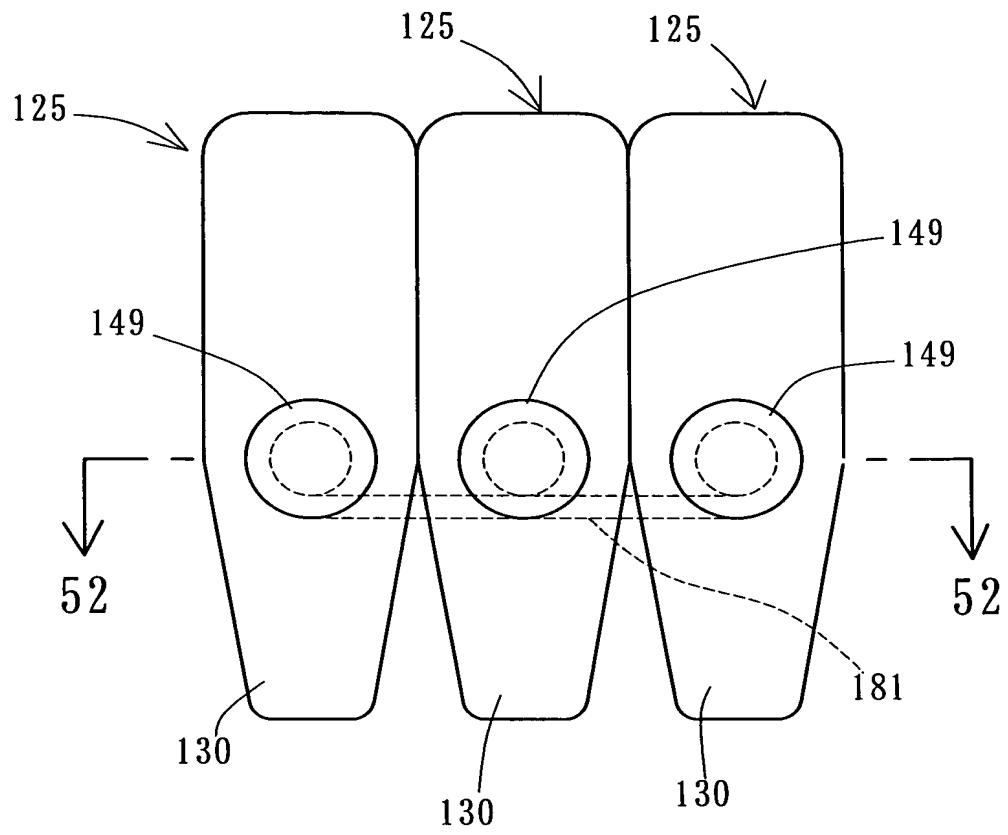


Fig. 50

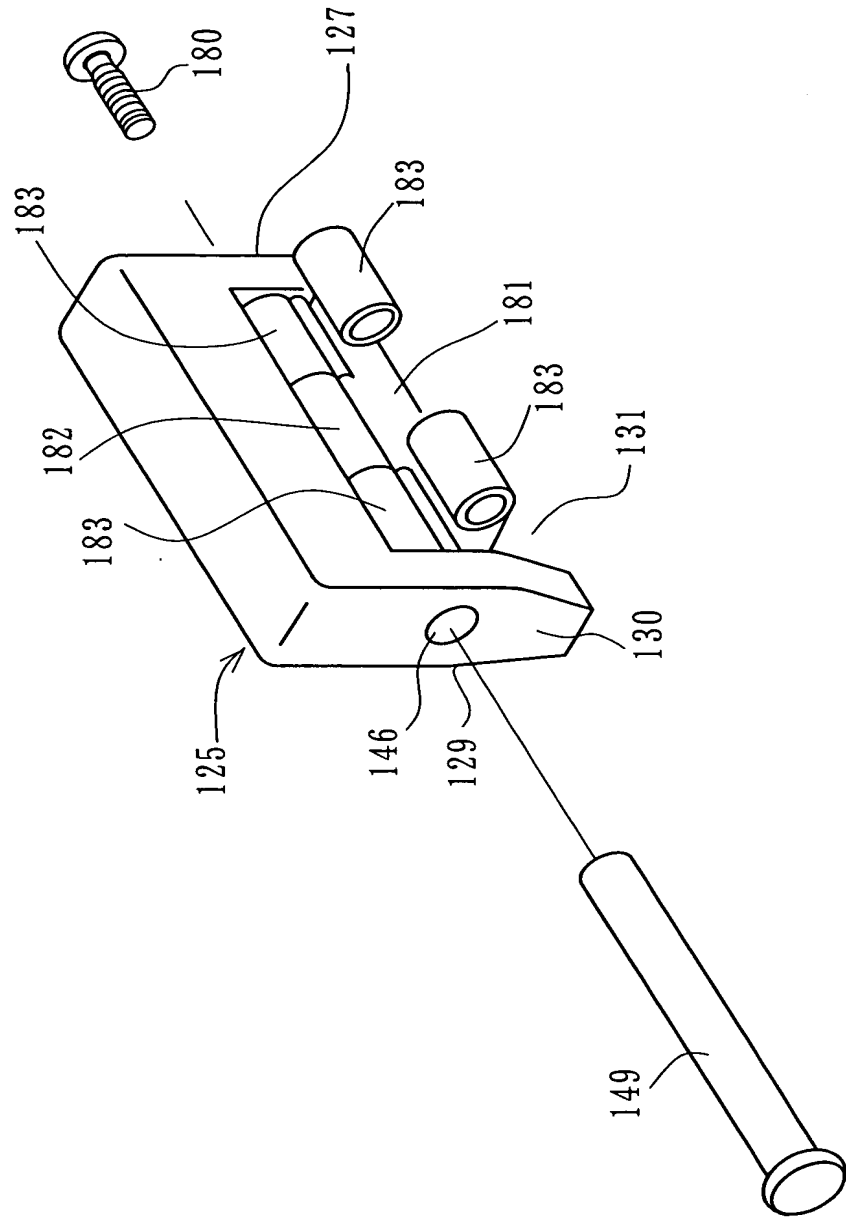
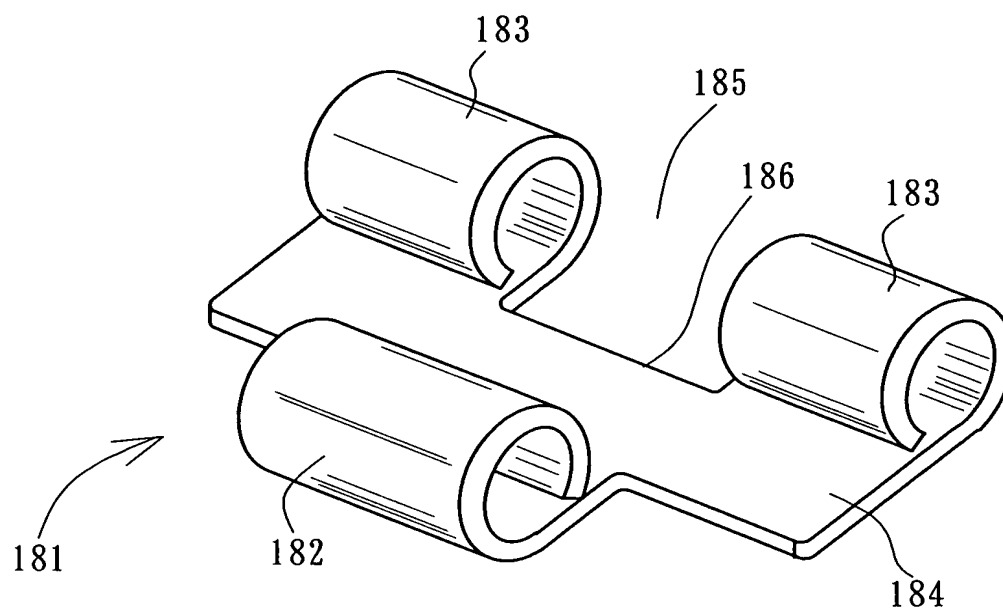
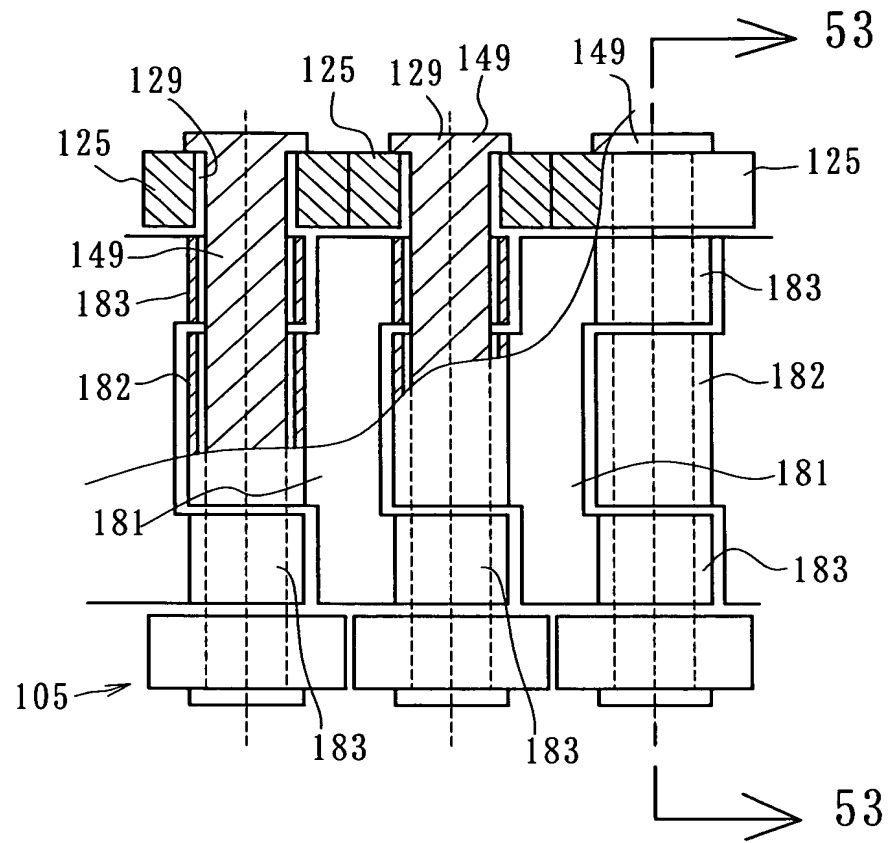


Fig. 5 1





# Fig. 5 2



# Fig. 5 3

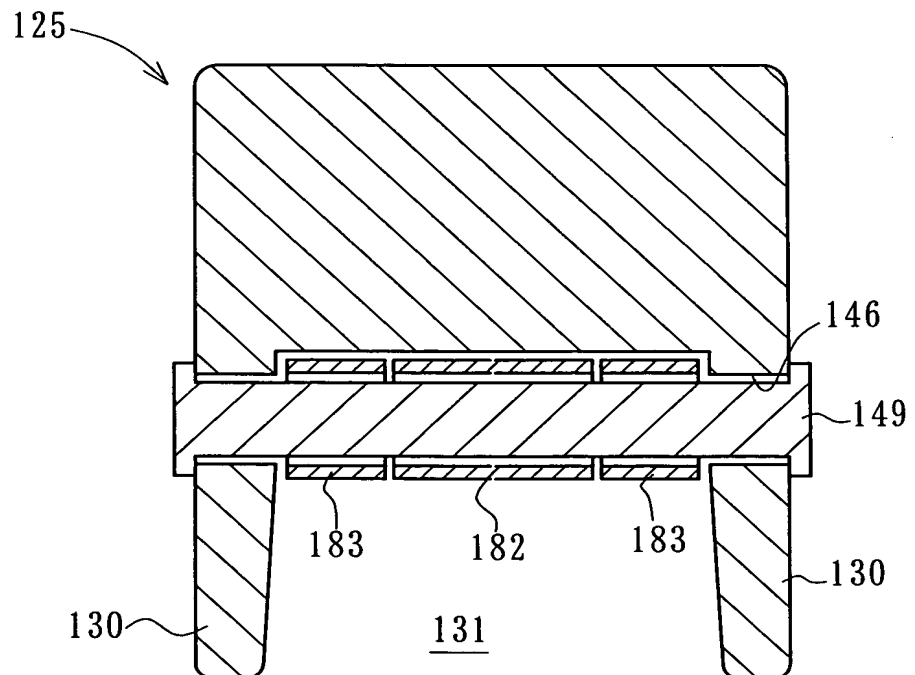


Fig. 5 4

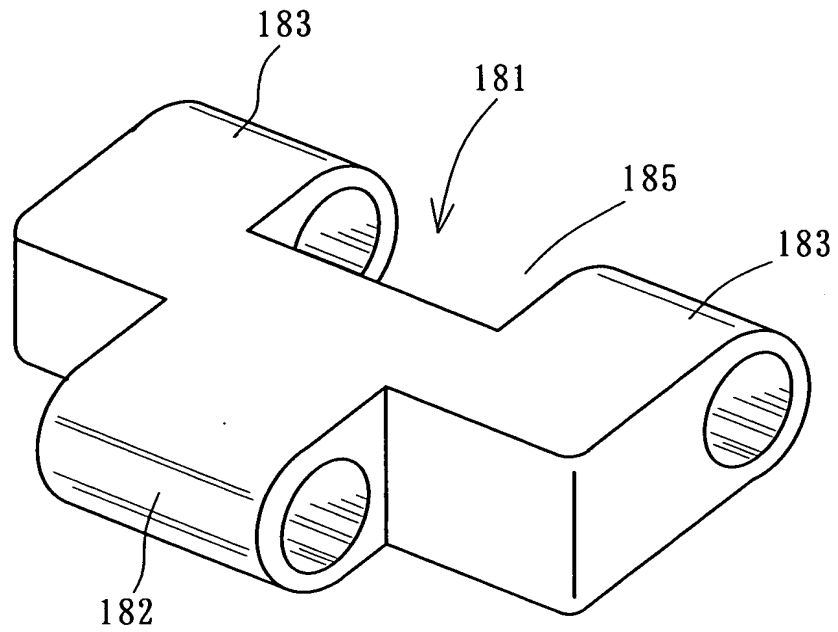


Fig. 5 5

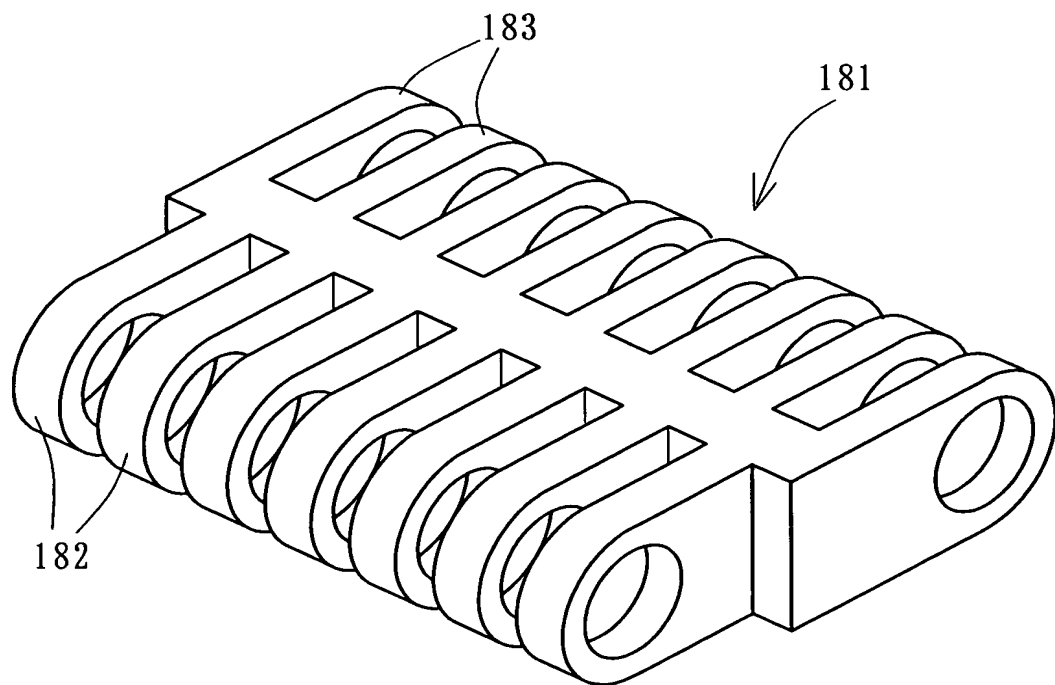


Fig. 5 6

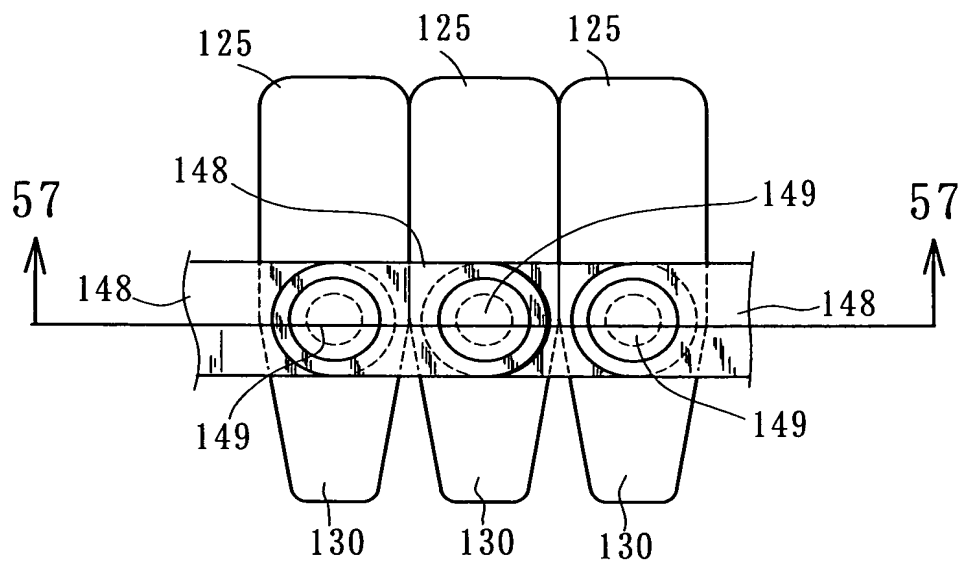


Fig. 5 7

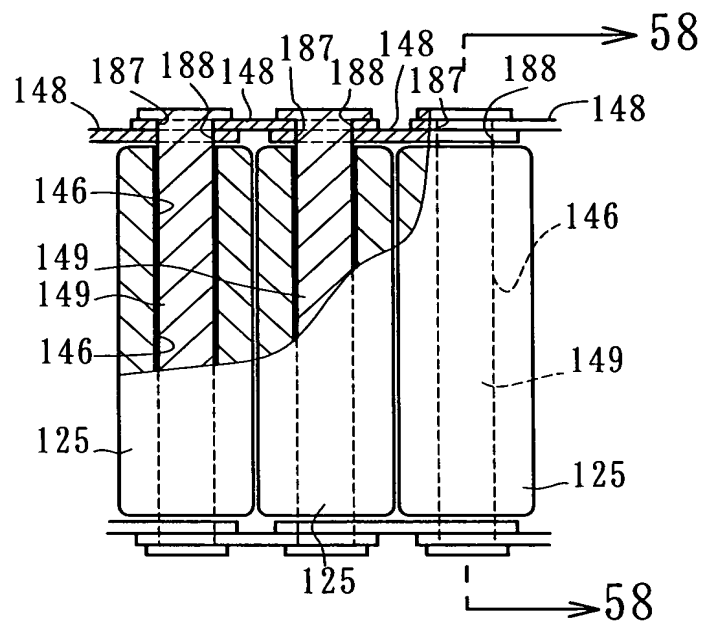


Fig. 5 8

